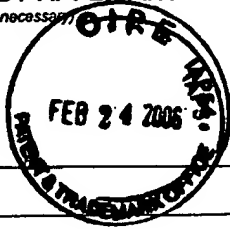


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STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	09/672,711
Filing Date	September 29, 2000
First Named Inventor	Burton, Thomas
Group Art Unit	2154
Examiner Name	Donaghue, Larry

Sheet 1 of 1

Attorney Docket No: 884.984US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
<i>[Signature]</i>	US-20030091037A1	05/15/2003	Latif, A. , et al.	04/30/2002
<i>[Signature]</i>	US-6,778,548	08/17/2004	Burton, T. E., et al.	06/26/2000
<i>[Signature]</i>	US-6,937,611	08/30/2005	Ward, K. A.	04/21/2000

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>[Signature]</i>		"InfiBand(TM) Architecture Specification", <u>InfiBand Trade Association</u> , Volume 1, Release 1.0, InfiBand Trade Association, (Oct. 24, 2000), 1-880	
<i>[Signature]</i>		"InfiBand(TM) Architecture Specification", <u>InfiBand Trade Association</u> , Volume 2, Release 1.0, InfiBand Trade Association, (Oct. 24, 2000), 1-623	
<i>[Signature]</i>		BEGEL, A. , et al., "An Analysis of VI Architecture Primitives in Support of Parallel and Distributed Communication", <u>Concurrency and Computation: Practice and Experience</u> , 14(1), (2001), 1-16	
<i>[Signature]</i>		BUONADONNA, P. , et al., "An Implementation and Analysis of the Virtual Interface Architecture", <u>IEEE/ACM Conference on Supercomputing</u> , SC98, (1998), 1-19	
<i>[Signature]</i>		DUNNING, D. , et al., "The Virtual Interface Architecture", <u>IEEE Micro</u> , 18(2), (1998), 66-76	
<i>[Signature]</i>		GARCIA, D. , et al., "Future I/O", <u>The 6th International Conference on Parallel Interconnects</u> , 1999. (PI '99) Proceedings., (Oct. 1999), 1-5	
<i>[Signature]</i>		MAHON, M. J., et al., "Hewlett-Packard Precision Architecture: The Processor", <u>Hewlett-Packard Journal</u> , (Aug. 1986), 4-22	
<i>[Signature]</i>		PATEL, N. , et al., "A model of completion queue mechanisms using the virtual interface API", <u>IEEE International Conference on Cluster Computing</u> , 2000. Proceedings., (2000), 280-288	

EXAMINER

DATE CONSIDERED

04/13/06

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 808. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached